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Substitute for form 1449/PTO				<b>Complete if Known</b>	
<b>FIRST SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>				Application Number	10/694,729
				Filing Date	October 29, 2003
				First Named Inventor	Hoang T. TRAN
				Art Unit	2182
				Examiner Name	Sun, Scott C.
Sheet	1	of	2	Attorney Docket Number	1875.4520000

U.S. PATENT DOCUMENTS					
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
SS	US1	4,627,070	12-02-1986	Champlin et al.	
	US2	4,833,695	05-23-1989	Greub	
	US3	4,967,201	10-30-1990	Rich, III	
	US4	5,726,991	03-10-1998	Chen et al.	
	US5	5,793,990	08-11-1998	Jirgal et al.	
	US6	5,933,021	08-03-1999	Mohd	
	US7	6,081,570	06-27-2000	Ghuman et al.	
	US8	6,140,956	10-31-2000	Hillman et al.	
	US9	6,259,693	07-10-2001	Ganmukhi et al.	
	US10	6,137,734	12-24-2000	Schoner et al.	
	US11	6,317,804 B1	11-13-2001	Levy et al.	
	US12	6,438,717 B1	08-20-2002	Butler et al.	
	US13	6,483,849 B1	11-19-2002	Bray et al.	
	US14	7,032,139 B1	04-18-2006	Iryami et al.	
	US15	7,035,228 B2	04-25-2006	Baumer	
	US16	2001/0012288 A1	08-09-2001	Yu	
	US17	2002/0054569 A1	05-09-2002	Morkawa	
	US18	2003/0009307 A1	01-09-2003	Mejia et al.	
	US19	2003/0048781 A1	03-13-2003	Peirson	
	US20	2004/0083077 A1	04-29-2004	Baumer et al.	

FOREIGN PATENT DOCUMENTS					
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)			
	FP1				
	FP2				
	FP3				
	FP4				
	FP5				
	FP6				
	FP7				
	FP8				
	FP9				

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
	NPL1	Copy of International Search Report Issued Dec. 14, 2004 for International Application No. PCT/US03/34234, 5 pages.	
	NPL2	Alaska Quad-Port Gigabit Ethernet Over Copper Transceivers, from <a href="http://www.marvell.com/products/transceivers/quadport/...">http://www.marvell.com/products/transceivers/quadport/...</a> , 1 page, printed May 8, 2007.	
	NPL3	Alaska X10 Gigabit Ethernet Alaska Gigabit Ethernet Fast Ethernet Physical Layer (PHY) Transceiver Families Provide a Full Range of Ethernet Transceiver Solutions for the Broadband Communications Industry, from <a href="http://www.nuhorizons.com/FeaturedProducts/Volume3/Marvell/phy_transceiver.asp">http://www.nuhorizons.com/FeaturedProducts/Volume3/Marvell/phy_transceiver.asp</a> , 4 pages, Copyright 2007, printed May 8, 2007.	
	NPL4	Cisco Search: Results for "cdl" within "News@Cisco", from <a href="http://www.cisco.com/pcgi-bin/search/search.pl">http://www.cisco.com/pcgi-bin/search/search.pl</a> , Copyright 1992-2003, printed July 29, 2003.	
	NPL5	Industry Breakthrough: Marvell Announces the First Quad-Port Transceiver to Support Both Copper and Fiber-Optic Gigabit Ethernet Interfaces, from Business Wire at <a href="http://www.encyclopedia.com/printable.aspx?id=1G1:68912211">www.encyclopedia.com/printable.aspx?id=1G1:68912211</a> , 4 pages, January 9, 2001, printed May 8, 2007.	
	NPL6	David Maliniak (ed.), Bel's integrated connector modules support Marvell's Alaska quad Gigabit Ethernet transceiver, from <a href="http://www.electronicweb.com/Content/news/...">http://www.electronicweb.com/Content/news/...</a> , 1 page, December 14, 2000, printed May 8, 2007.	
	NPL7	Marvell Gets Small, from <a href="http://www.lightreading.com/document.asp?doc_id=12004&amp;print=true">http://www.lightreading.com/document.asp?doc_id=12004&amp;print=true</a> , 1 page, February 19, 2002, printed May 8, 2007.	
	NPL8	Marvell Introduces the Industry's Smallest Quad-Port Gigabit Transceiver Device, Enabling Ultra High Port Density Enterprise Switching Systems, from <a href="http://www.marvell.com/press/pressNewsDisplay.do?releaseID=41">http://www.marvell.com/press/pressNewsDisplay.do?releaseID=41</a> , 3 pages, February 19, 2002, printed May 8, 2007.	
	NPL9	Ed Turner, David Law, Sep. 17-19, 2001, "IEEE P802.3ae MDC/MDIO", <a href="http://www.ieee802.org/3/efm/public/sep01/turner.sub.-1.sub.-0901.pdf">http://www.ieee802.org/3/efm/public/sep01/turner.sub.-1.sub.-0901.pdf</a> (copy attached).	
	NPL10	Q&A: Hiroshi Suzuki on Extending Ethernet Beyond the LAN, from <a href="http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_qa.html">http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_qa.html</a> , 3 pages, Copyright 1992-2003, printed July 29, 2003.	

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	NPL11	IEEE Std. 802.3, Part 3: Carrier Sense Multiple Access with Collision Detection (CSMA/CD) access method and physical layer specifications, Section Two, pp. 9-55, Copyright 2002.	
	NPL12	IEEE Std. 802.3, Part 3: Carrier Sense Multiple Access with Collision Detection (CSMA/CD) access method and physical layer specifications, Section Four, pp. 9-152, Copyright 2005.	
	NPL13	Cisco Systems, Hiroshi Suzuki: Taking Ethernet Beyond the LAN, <a href="http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_profile.html">http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_profile.html</a>	
	NPL14	Cisco Systems, Q&A: Hiroshi Suzuki on Extending Ethernet Beyond the LAN, <a href="http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_qa.html">http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_qa.html</a>	
	NPL15	Cisco Systems, Innovators Profiles, <a href="http://newsroom.cisco.com/dlls/innovators/inn_profiles.html">http://newsroom.cisco.com/dlls/innovators/inn_profiles.html</a>	
	NPL16	Cisco Systems, Optical, <a href="http://newsroom.cisco.com/dlls/innovatros/optical/">http://newsroom.cisco.com/dlls/innovatros/optical/</a>	
	NPL17	Cisco Systems, Cisco Systems ad Aretae Interactive Launch Internet Powered Property (IP2), <a href="http://newroom.cisco.com/dlls/global/asiapac/news/2000/pr_07-25.html">http://newroom.cisco.com/dlls/global/asiapac/news/2000/pr_07-25.html</a>	
	NPL18		
	NPL19		
	NPL20		

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